**\*Probably format at Calibri font 12 to make it look nice af\***

**Title Page (format as necessary, probably big and at the center)**

Jason Arkin, Ian Feeney, Sebastian Rodriguez, Spencer Williams, Sebastian Witkowski

EECS 392: VLSI / FPGA System Design Projects

06/12/2015

Instructor: David C. Zaretsky, PhD

**Front Matter (apparently this is a table of content, figures, tables, etc. like DTC)**

**Executive Summary (a high level summary of the project, problem, design, benefits and limitations)**

For our undergraduate capstone project, we decided to undertake audio wave modulation using hardware through a Field Programmable Gate Array.

**Body (this is the main chunk of shit)**

*Introduction*

*Broader Considerations*

*Design Constraints and Requirements*

*Design Description*

*Design Optimization*

*Testing / Simulation*

*Implementation / Synthesis*

*Conclusion*

**References (the things we referred to when coming up with the design, a lot of this stuff is on the facebook chat)**

**Appendices (extra details that would break flow of the report, extra code, tables, samples etc.)**